Fig.1
PRIOR ART

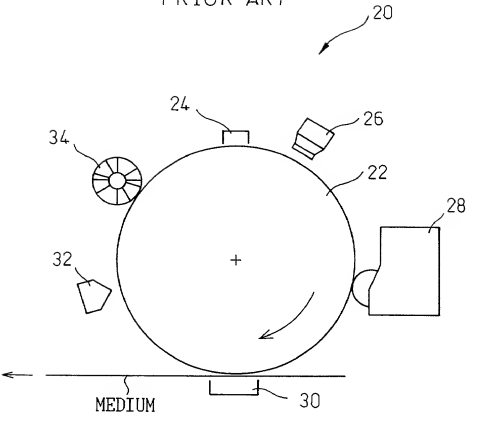


Fig.2
PRIOR ART

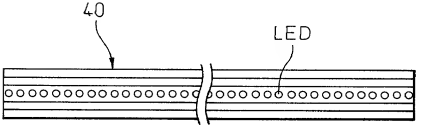


Fig.3

PRIOR ART

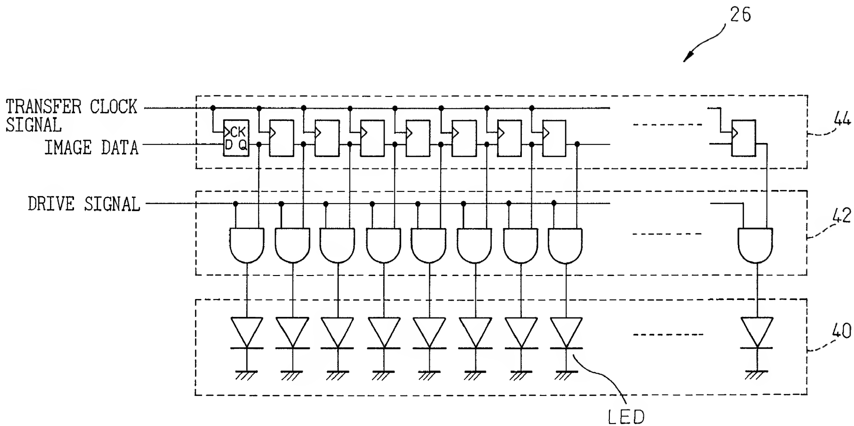


Fig.4

PRIOR ART

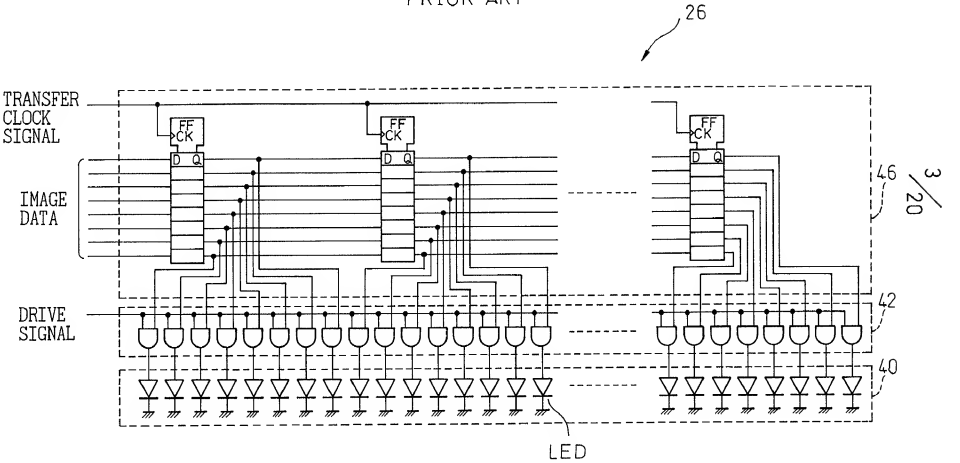


Fig.5
PRIOR ART

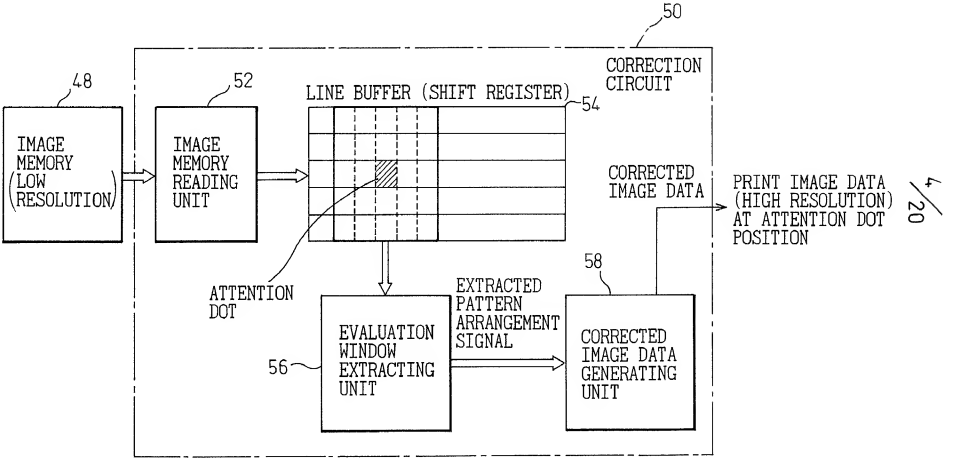
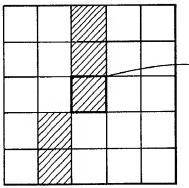


Fig.6A
PRIOR ART

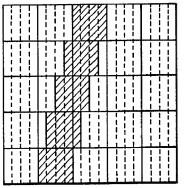


DOT ARRANGEMENT IN EVALUATION WINDOW

Fig.6B
PRIOR ART

RESULT OF EVALUATION/ CORRECTION AT ATTENTION DOT POSITION

Fig.6C
PRIOR ART



RESULT OF CORRECTION (ENTIRE REGION)

Fig.7
PRIOR ART

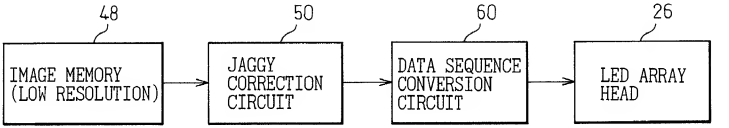


Fig.8

PRIOR ART

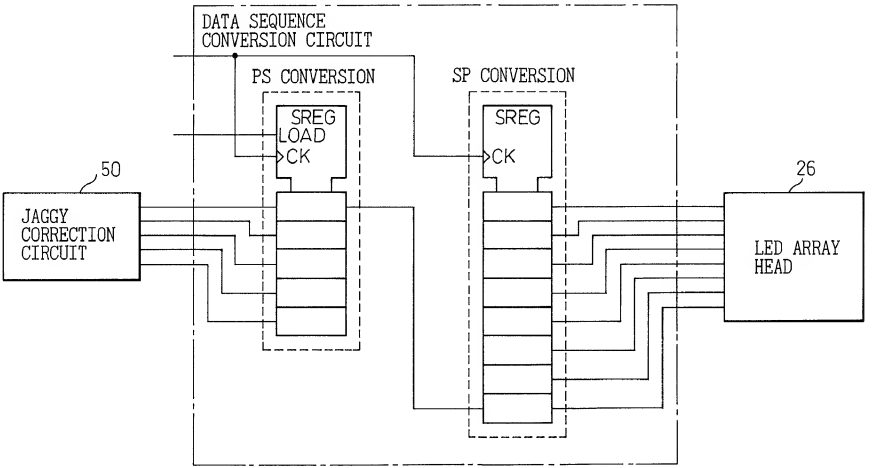


Fig.9

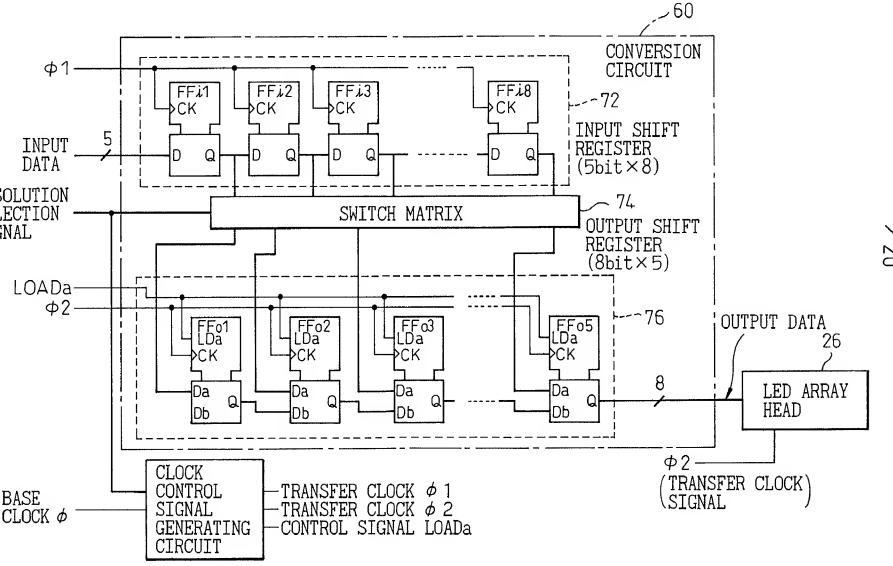


Fig.10

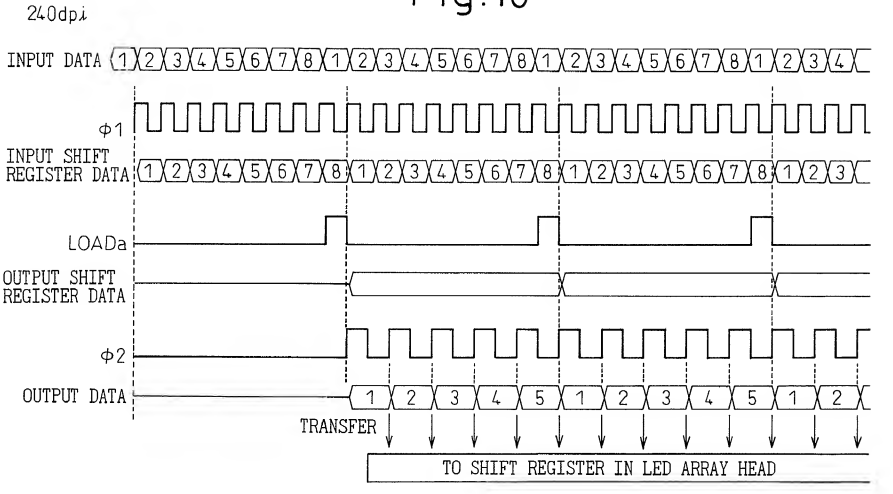


Fig.11

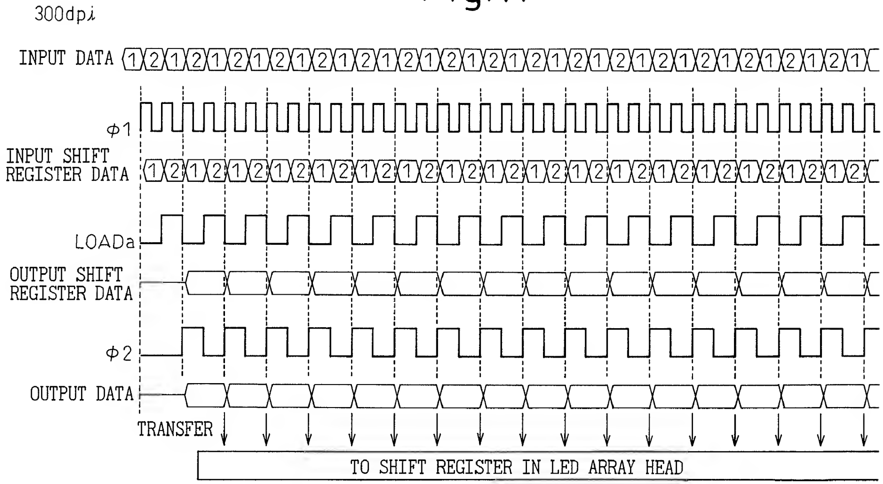

300dpi
INPUT DATA
φ1
INPUT SHIFT REGISTER DATA
LOADa
OUTPUT SHIFT REGISTER DATA
φ2
OUTPUT DATA
TRANSFER
TO SHIFT REGISTER IN LED ARRAY HEAD

Fig.12

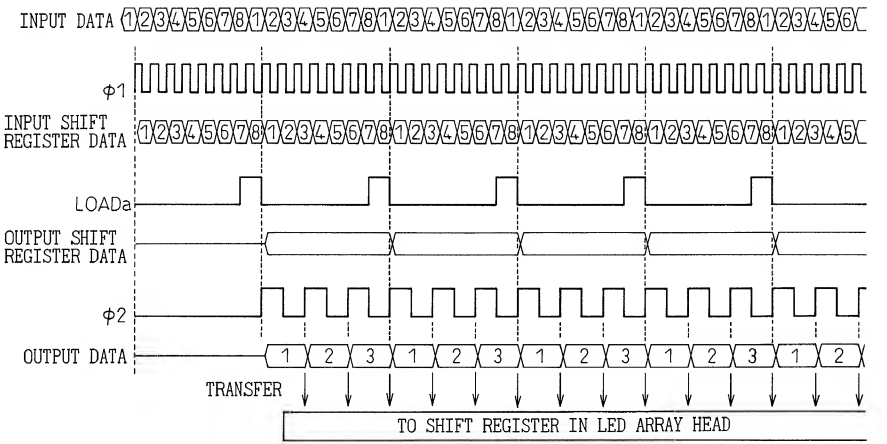


Fig. 13

OUTPUT TERMINAL (DESTINATION)	FFo1							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	FFi2 -Q2	FFi2 -Q1	FFi2 -Q0	FFi1 -Q4	FFi1 -Q3	FFi1 -Q2	FFi1 -Q1	FFi1 -Q0

OUTPUT TERMINAL (DESTINATION)	FFo2							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	FFi4 -Q0	FFi3 -Q4	FFi3 -Q3	FFi3 -Q2	FFi3 -Q1	FFi3 -Q0	FFi2 -Q4	FFi2 -Q3

OUTPUT TERMINAL (DESTINATION)	FFo3							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	FFi5 -Q3	FFi5 -Q2	FFi5 -Q1	FFi5 -Q0	FFi4 -Q4	FFi4 -Q3	FFi4 -Q2	FFi4 -Q1

OUTPUT TERMINAL (DESTINATION)	FFo4							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	FFi7 -Q1	FFi7 -Q0	FFi6 -Q4	FFi6 -Q3	FFi6 -Q2	FFi6 -Q1	FFi6 -Q0	FFi5 -Q4

OUTPUT TERMINAL (DESTINATION)	FFo5							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	FFi8 -Q4	FFi8 -Q3	FFi8 -Q2	FFi8 -Q1	FFi8 -Q0	FFi7 -Q4	FFi7 -Q3	FFi7 -Q2

Fig.14

OUTPUT TERMINAL (DESTINATION)	FFo1							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	×	×	×	×	×	×	×	×

OUTPUT TERMINAL (DESTINATION)	FFo2							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	×	×	×	×	×	×	×	×

OUTPUT TERMINAL (DESTINATION)	FFo3							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	×	×	×	×	×	×	×	×

OUTPUT TERMINAL (DESTINATION)	FFo4							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	×	×	×	×	×	×	×	×

OUTPUT TERMINAL (DESTINATION)	FFo5							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	FFi2 -Q3	FFi2 -Q2	FFi2 -Q1	FFi2 -Q0	FFi1 -Q3	FFi1 -Q2	FFi1 -Q1	FFi1 -Q0

Fig. 15

OUTPUT TERMINAL (DESTINATION)	FFo1							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	×	×	×	×	×	×	×	×

OUTPUT TERMINAL (DESTINATION)	FFo2							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	×	×	×	×	×	×	×	×

OUTPUT TERMINAL (DESTINATION)	FFo3							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	FFi3 -Q1	FFi3 -Q0	FFi2 -Q2	FFi2 -Q1	FFi2 -Q0	FFi1 -Q2	FFi1 -Q1	FFi1 -Q0

OUTPUT TERMINAL (DESTINATION)	FFo4							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	FFi6 -Q0	FFi5 -Q2	FFi5 -Q1	FFi5 -Q0	FFi4 -Q2	FFi4 -Q1	FFi4 -Q0	FFi3 -Q2

OUTPUT TERMINAL (DESTINATION)	FFo5							
	-Da7	-Da6	-Da5	-Da4	-Da3	-Da2	-Da1	-Da0
OUTPUT SIGNAL	FFi8 -Q2	FFi8 -Q1	FFi8 -Q0	FFi7 -Q2	FFi7 -Q1	FFi7 -Q0	FFi6 -Q2	FFi6 -Q1

Fig.16A

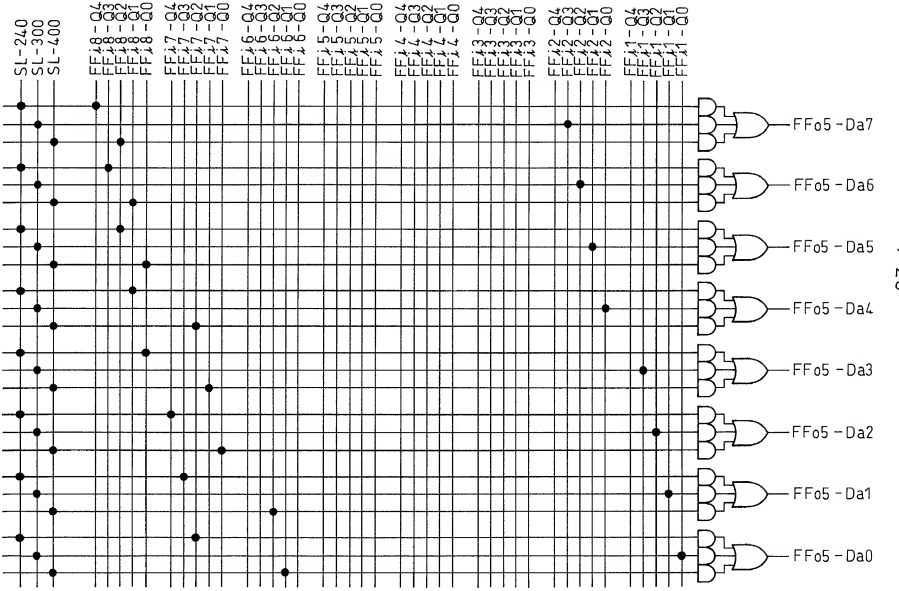


Fig.16B

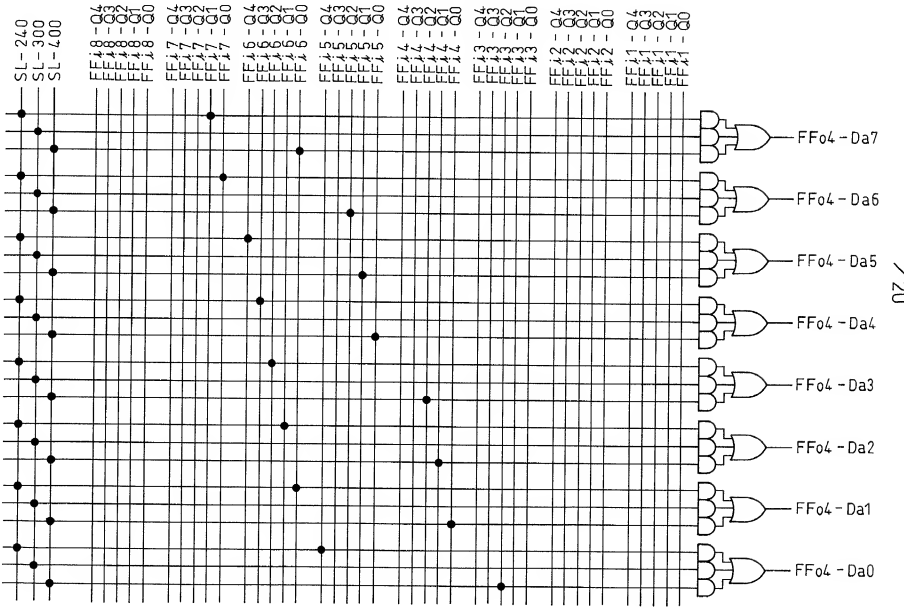


Fig. 16C

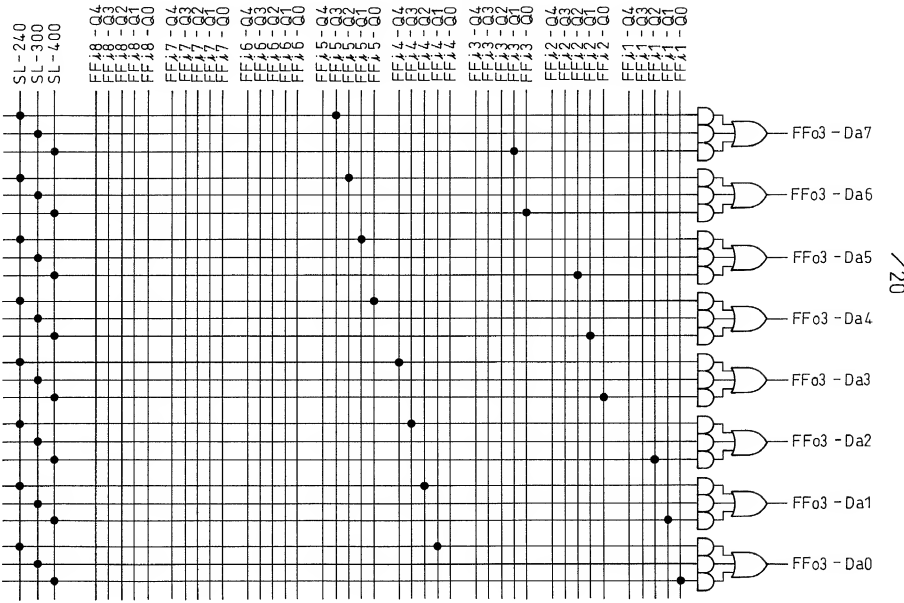


Fig.16D

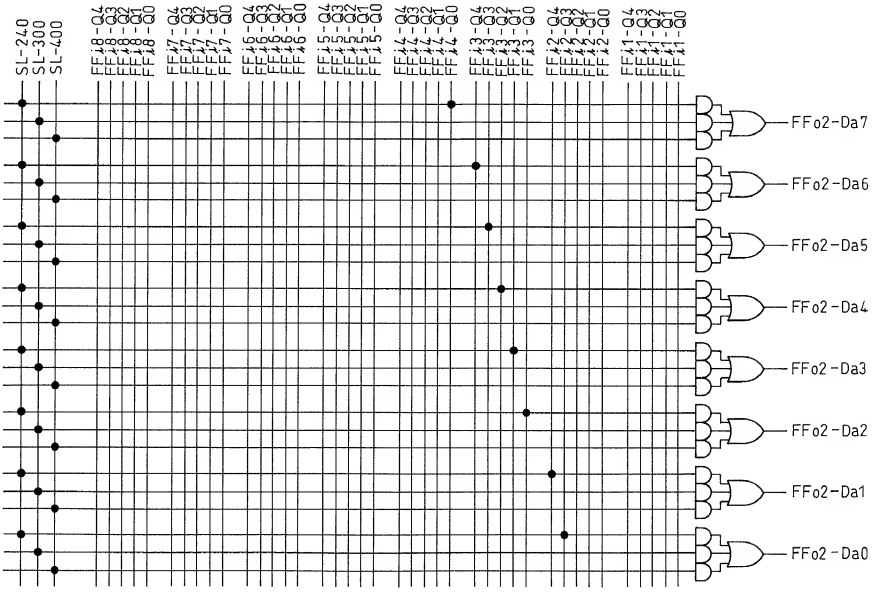


Fig.16E

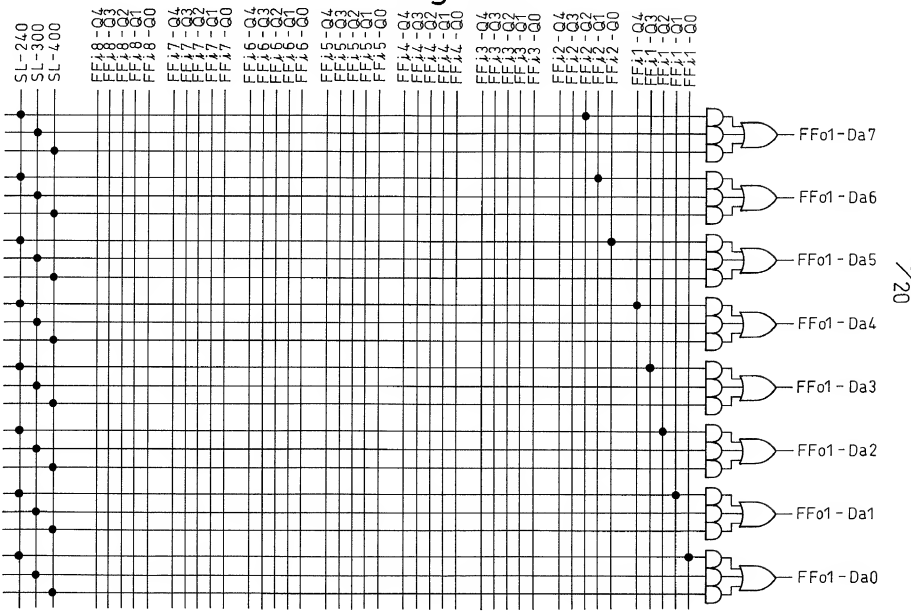


Fig.17A

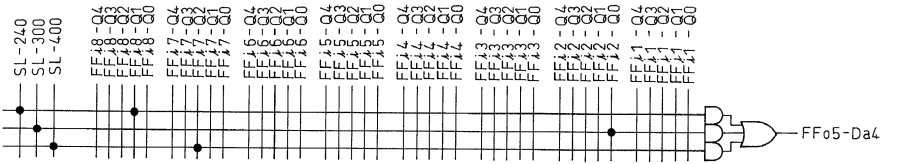


Fig.17B